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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
10/734,624	12/15/2003	Kevin Lloyd-Jones	1509-236A	6585

7590 08/30/2004

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EXAMINER

CHACE, CHRISTIAN

ART UNIT

PAPER NUMBER

2187

DATE MAILED: 08/30/2004

Please find below and/or attached an Office communication concerning this application or proceeding.

FEB 2

Office Action Summary	Application No.	Applicant(s)
	10/734,624	LLOYD-JONES, KEVIN
	Examiner	Art Unit
	Christian P. Chace	2187

-- The MAILING DATE of this communication appears on the cover sheet with the correspondence address --

Period for Reply

A SHORTENED STATUTORY PERIOD FOR REPLY IS SET TO EXPIRE 3 MONTH(S) FROM THE MAILING DATE OF THIS COMMUNICATION.

- Extensions of time may be available under the provisions of 37 CFR 1.136(a). In no event, however, may a reply be timely filed after SIX (6) MONTHS from the mailing date of this communication.
- If the period for reply specified above is less than thirty (30) days, a reply within the statutory minimum of thirty (30) days will be considered timely.
- If NO period for reply is specified above, the maximum statutory period will apply and will expire SIX (6) MONTHS from the mailing date of this communication.
- Failure to reply within the set or extended period for reply will, by statute, cause the application to become ABANDONED (35 U.S.C. § 133). Any reply received by the Office later than three months after the mailing date of this communication, even if timely filed, may reduce any earned patent term adjustment. See 37 CFR 1.704(b).

Status

- 1) Responsive to communication(s) filed on 15 December 2003.
 2a) This action is FINAL. 2b) This action is non-final.
 3) Since this application is in condition for allowance except for formal matters, prosecution as to the merits is closed in accordance with the practice under *Ex parte Quayle*, 1935 C.D. 11, 453 O.G. 213.

Disposition of Claims

- 4) Claim(s) 1-6 and 8 is/are pending in the application.
 4a) Of the above claim(s) _____ is/are withdrawn from consideration.
 5) Claim(s) _____ is/are allowed.
 6) Claim(s) 1-6,8 is/are rejected.
 7) Claim(s) _____ is/are objected to.
 8) Claim(s) _____ are subject to restriction and/or election requirement.

Application Papers

- 9) The specification is objected to by the Examiner.
 10) The drawing(s) filed on 15 December 2003 is/are: a) accepted or b) objected to by the Examiner.
 Applicant may not request that any objection to the drawing(s) be held in abeyance. See 37 CFR 1.85(a).
 Replacement drawing sheet(s) including the correction is required if the drawing(s) is objected to. See 37 CFR 1.121(d).
 11) The oath or declaration is objected to by the Examiner. Note the attached Office Action or form PTO-152.

Priority under 35 U.S.C. § 119

- 12) Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f).
 a) All b) Some * c) None of:
 1. Certified copies of the priority documents have been received.
 2. Certified copies of the priority documents have been received in Application No. _____.
 3. Copies of the certified copies of the priority documents have been received in this National Stage application from the International Bureau (PCT Rule 17.2(a)).

* See the attached detailed Office action for a list of the certified copies not received.

Attachment(s)

- | | |
|---|---|
| 1) <input type="checkbox"/> Notice of References Cited (PTO-892) | 4) <input type="checkbox"/> Interview Summary (PTO-413) |
| 2) <input type="checkbox"/> Notice of Draftsperson's Patent Drawing Review (PTO-948) | Paper No(s)/Mail Date. _____ |
| 3) <input type="checkbox"/> Information Disclosure Statement(s) (PTO-1449 or PTO/SB/08)
Paper No(s)/Mail Date _____. | 5) <input type="checkbox"/> Notice of Informal Patent Application (PTO-152) |
| | 6) <input type="checkbox"/> Other: _____. |

DETAILED ACTION

Response to Amendment

This Office action has been issued in response to Preliminary Amendment filed 12/15/03. Claims 1-6 and 8 are pending. Claims 7 and 9 are canceled.

Drawings

The drawings are objected to under 37 CFR 1.83(a). The drawings must show every feature of the invention specified in the claims. Therefore, the lookup address being greater than OR EQUAL TO said requested logical address must be shown or the feature(s) canceled from the claim(s). No new matter should be entered.

Corrected drawing sheets in compliance with 37 CFR 1.121(d) are required in reply to the Office action to avoid abandonment of the application. Any amended replacement drawing sheet should include all of the figures appearing on the immediate prior version of the sheet, even if only one figure is being amended. The figure or figure number of an amended drawing should not be labeled as "amended." If a drawing figure is to be canceled, the appropriate figure must be removed from the replacement sheet, and where necessary, the remaining figures must be renumbered and appropriate changes made to the brief description of the several views of the drawings for consistency. Additional replacement sheets may be necessary to show the renumbering of the remaining figures. The replacement sheet(s) should be labeled "Replacement Sheet" in the page header (as per 37 CFR 1.84(c)) so as not to obstruct any portion of the drawing figures. If the changes are not accepted by the examiner, the applicant will

be notified and informed of any required corrective action in the next Office action. The objection to the drawings will not be held in abeyance.

Claim Rejections - 35 USC § 112

The following is a quotation of the first paragraph of 35 U.S.C. 112:

The specification shall contain a written description of the invention, and of the manner and process of making and using it, in such full, clear, concise, and exact terms as to enable any person skilled in the art to which it pertains, or with which it is most nearly connected, to make and use the same and shall set forth the best mode contemplated by the inventor of carrying out his invention.

Claim 8 is rejected under 35 U.S.C. 112, first paragraph, as failing to comply with the written description requirement. The claim(s) contains subject matter which was not described in the specification in such a way as to reasonably convey to one skilled in the relevant art that the inventor(s), at the time the application was filed, had possession of the claimed invention. The lookup address being greater than OR EQUAL TO the requested logical address is new matter. The specification discloses the logical address being less than or equal to the requested logical address on page 8, in line 17, which recites the rest of the instant claim limitation verbatim.

Claim Rejections - 35 USC § 102

The following is a quotation of the appropriate paragraphs of 35 U.S.C. 102 that form the basis for the rejections under this section made in this Office action:

A person shall be entitled to a patent unless –

(b) the invention was patented or described in a printed publication in this or a foreign country or in public use or on sale in this country, more than one year prior to the date of application for patent in the United States.

Claims 1-5 are rejected under 35 U.S.C. 102(b) as being anticipated by Farmwald et al (US Patent #6,445,613).

With respect to claim 1, a plurality of arrays of physical memory storage elements each having a corresponding respective physical address (inherent) each said memory storage element capable of storing at least one bit of data (inherent), address conversion means for converting between a plurality of logical addresses and a said plurality of physical addresses, wherein said address conversion means comprises at least one memory area storing address translation data "describing" a plurality of ranges of logical addresses, and for each said range of logical addresses a corresponding respective range of said physical addresses is disclosed in column 7, lines 60-68 as a TLB.

With respect to claim 2, a plurality of data entries, each describing the "first" physical address of a plurality of continuously addressed physical memory elements, said physical address data being specified in terms of a block address data describing a block of physical addresses, and each describing at least one logical address corresponding to said physical address is disclosed in column 8, line 1. A row address data describing a row of individual memory components, each said memory component comprising an array of a plurality of said memory elements is disclosed in figure 1, "row decode: inherently uses row addresses. A segment address data, said segment address data specifying a segment of each of a plurality of segments of a plurality of said components is disclosed in column 7, lines 26-27, and a relationship between adjacent data entries is disclosed in column 7, lines 19-22, which defines ranges of said logical addresses.

With respect to claim 3, the address translation data being stored at least once within said memory storage elements of said storage device is disclosed in column 7, lines 60-68 – again, in the TLB, by definition.

With respect to claim 4, the plurality of arrays of memory storage elements comprising a plurality of individual memory storage components, each comprising a corresponding respective array of memory storage elements, the plurality of memory components arranged in a component array on one or more circuit boards, and connected together by a common physical address bus is disclosed in figure 2, DRAM #13, #14 and figure 3, #15-#17.

With respect to claim 5, a plurality of data entries each defining “a relationship” between the logical address and [a] sic the physical address, wherein each row is indexed by a separate index number is disclosed in column 8, lines 60-68 as a “row address” being an index to the information stored in that line, by definition of a row address.

Claim Rejections - 35 USC § 103

The following is a quotation of 35 U.S.C. 103(a) which forms the basis for all obviousness rejections set forth in this Office action:

(a) A patent may not be obtained though the invention is not identically disclosed or described as set forth in section 102 of this title, if the differences between the subject matter sought to be patented and the prior art are such that the subject matter as a whole would have been obvious at the time the invention was made to a person having ordinary skill in the art to which said subject matter pertains. Patentability shall not be negated by the manner in which the invention was made.

Claim 6 is rejected under 35 U.S.C. 103(a) as being unpatentable over Farmwald et al (discussed supra) and Nagai (US Patent #6,445,613).

Art Unit: 2187

Farmwald et al disclose the subject matter claimed in the claims upon which the instant claim depends.

The difference between the instant claim and Farmwald et al is the memory storage elements comprising magnetic RAM elements.

Nagai discloses a Magnetic RAM.

Therefore, it would have been obvious to one of ordinary skill in the art at the time of the invention, having the teachings of Farmwald et al and Nagai before him/her, to utilize the MRAM of Nagai in the system of Farmwald et al because the MRAM results in a reduction in the number of necessary components and a considerable enhancement in the degree of integration of the memory, as discussed by Nagai in the abstract.

Conclusion

Any inquiry concerning this communication or earlier communications from the examiner should be directed to Christian P. Chace whose telephone number is 703.306.5903. The examiner can normally be reached on 9-4-5.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Donald Sparks can be reached on 703.308.1756. The fax phone number for the organization where this application or proceeding is assigned is 703-872-9306.

Art Unit: 2187

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see <http://pair-direct.uspto.gov>. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free).



Christian P. Chace